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**AMERICAN INTERNATIONAL UNIVERSITY–BANGLADESH (AIUB)**

**DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING**

**DIGITAL LOGIC AND CIRCUITS LABORATORY**

**FALL 2023-2024**

**Section: Q, Group: 2, Exp No: 10**

**LAB REPORT ON**

Design of a Digital to Analog converter.

**Supervised By**

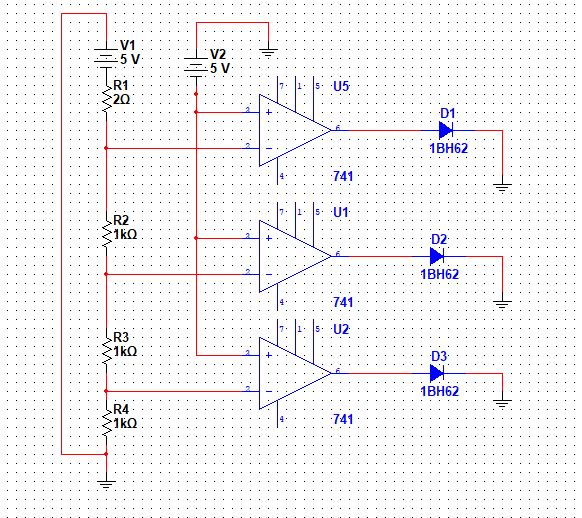
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Date of Submission: **December 3th, 2023**

**Simulations:**



**Figure-1:** R/2R Ladder DAC

A diagram of a circuit

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**Fig 2:** W2R Ladder DAC

A diagram of a circuit

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**Fig 2**: W2R Ladder DAC